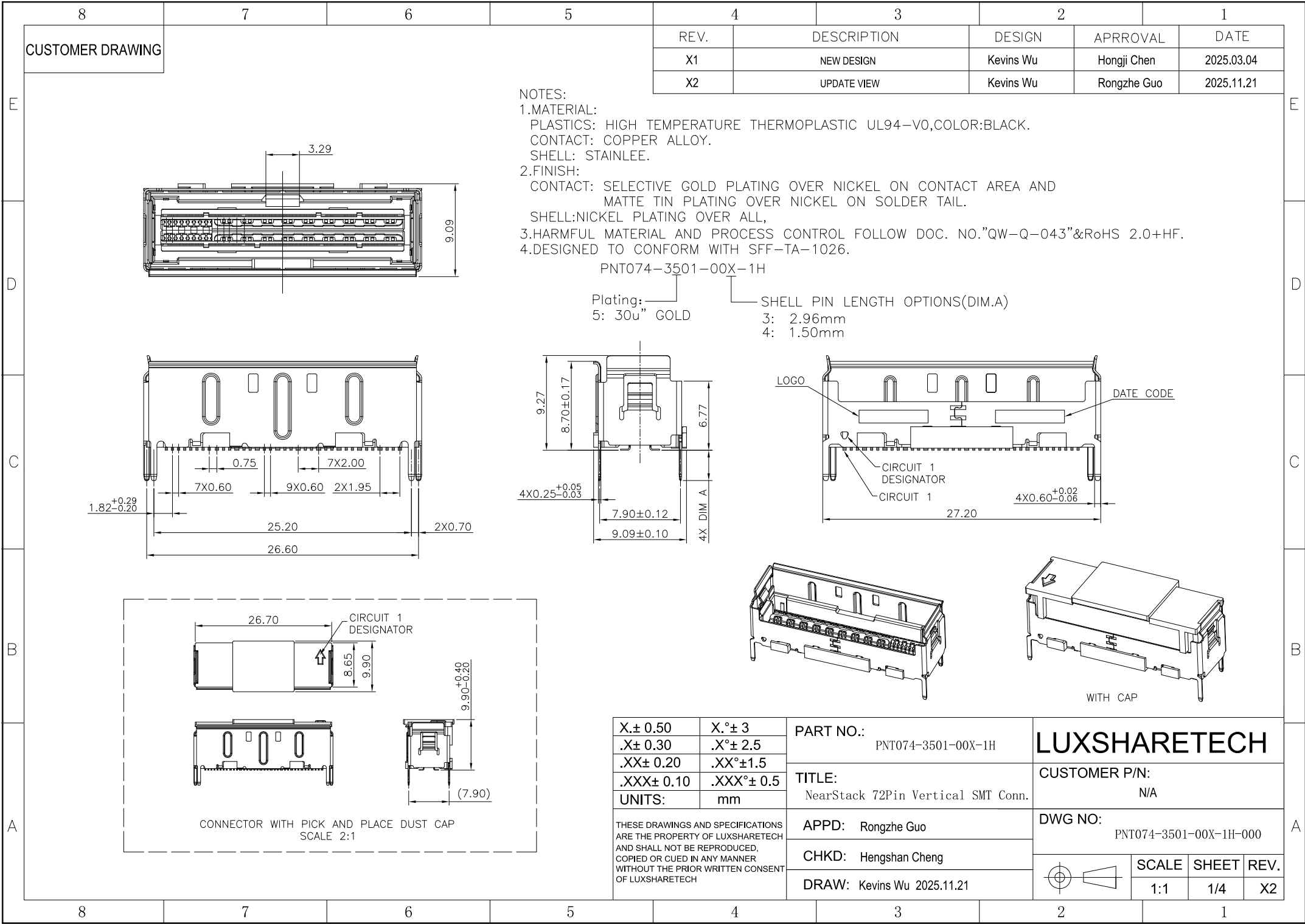




NOTES:

1.MATERIAL:

PLASTICS: HIGH TEMPERATURE THERMOPLASTIC UL94-V0,COLOR:BLACK.

CONTACT: COPPER ALLOY.

SHELL: STAINLEE.

2.FINISH:

CONTACT: SELECTIVE GOLD PLATING OVER NICKEL ON CONTACT AREA AND

MATTE TIN PLATING OVER NICKEL ON SOLDER TAIL.

SHELL:NICKEL PLATING OVER ALL,

3.HARMFUL MATERIAL AND PROCESS CONTROL FOLLOW DOC. NO."QW-Q-043"&RoHS 2.0+HF.

4.DESIGNED TO CONFORM WITH SFF-TA-1026.

PNT074-3501-00X-1H

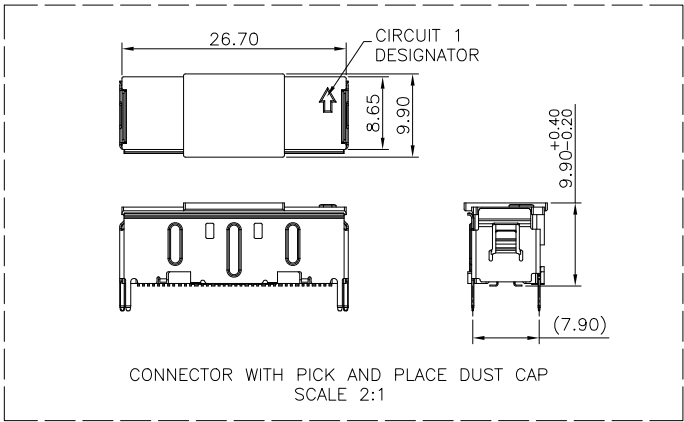
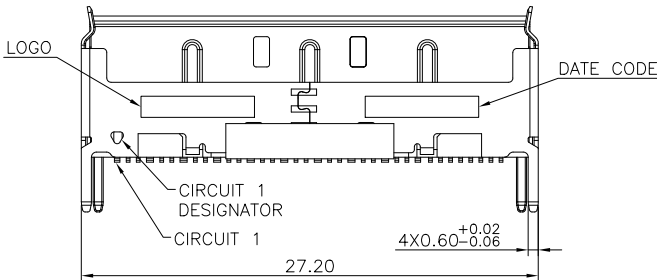
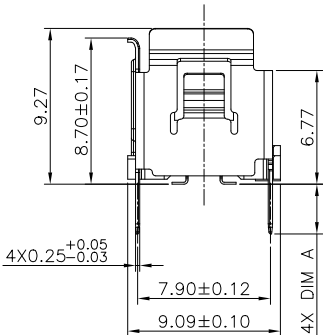
Plating:

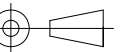
5: 30u" GOLD

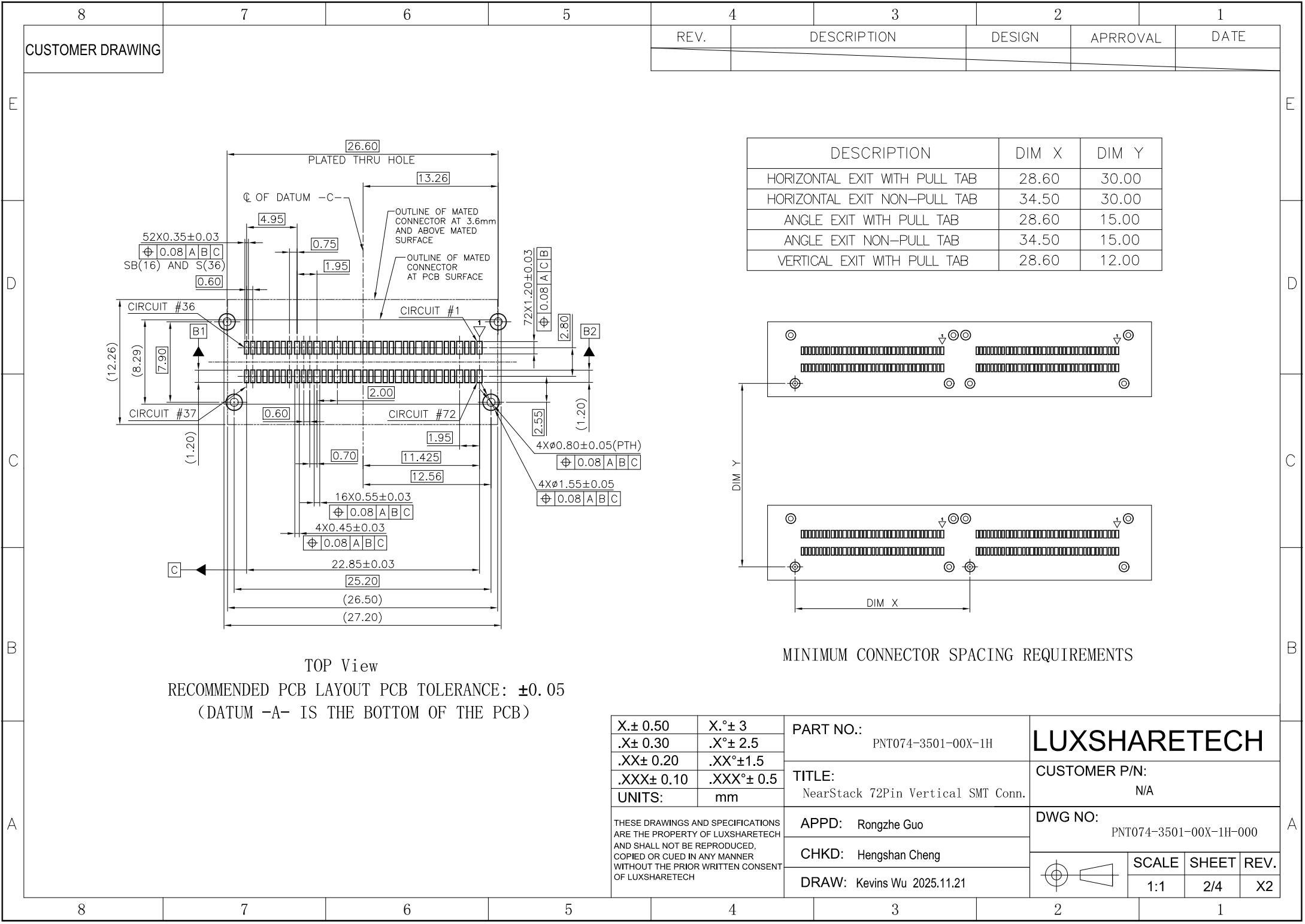
SHELL PIN LENGTH OPTIONS(DIM.A)

3: 2.96mm

4: 1.50mm



X.± 0.50	X.°± 3	PART NO.: PNT074-3501-00X-1H	LUXSHARETECH			
.X± 0.30	.X°± 2.5					
.XX± 0.20	.XX°± 1.5	TITLE: NearStack 72Pin Vertical SMT Conn.	CUSTOMER P/N: N/A			
.XXX± 0.10	.XXX°± 0.5					
UNITS:	mm	APPD: Rongzhe Guo	DWG NO: PNT074-3501-00X-1H-000			
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		CHKD: Hengshan Cheng		SCALE	SHEET	REV.
		DRAW: Kevins Wu 2025.11.21		1:1	1/4	X2



8		7		6		5		4		3		2		1																																																																																																																																																						
CUSTOMER DRAWING		PIN MAP: <table><tr><td>CIRCUIT #</td><td></td><td></td><td>CIRCUIT #</td></tr><tr><td>1</td><td>GND</td><td>GND</td><td>72</td></tr><tr><td>2</td><td>S</td><td>S</td><td>71</td></tr><tr><td>3</td><td>S</td><td>S</td><td>70</td></tr><tr><td>4</td><td>GND</td><td>GND</td><td>69</td></tr><tr><td>5</td><td>S</td><td>S</td><td>68</td></tr><tr><td>6</td><td>S</td><td>S</td><td>67</td></tr><tr><td>7</td><td>GND</td><td>GND</td><td>66</td></tr><tr><td>8</td><td>S</td><td>S</td><td>65</td></tr><tr><td>9</td><td>S</td><td>S</td><td>64</td></tr><tr><td>10</td><td>GND</td><td>GND</td><td>63</td></tr><tr><td>11</td><td>S</td><td>S</td><td>62</td></tr><tr><td>12</td><td>S</td><td>S</td><td>61</td></tr><tr><td>13</td><td>GND</td><td>GND</td><td>60</td></tr><tr><td>14</td><td>S</td><td>S</td><td>59</td></tr><tr><td>15</td><td>S</td><td>S</td><td>58</td></tr><tr><td>16</td><td>GND</td><td>GND</td><td>57</td></tr><tr><td>17</td><td>S</td><td>S</td><td>56</td></tr><tr><td>18</td><td>S</td><td>S</td><td>55</td></tr><tr><td>19</td><td>GND</td><td>GND</td><td>54</td></tr><tr><td>20</td><td>S</td><td>S</td><td>53</td></tr><tr><td>21</td><td>S</td><td>S</td><td>52</td></tr><tr><td>22</td><td>GND</td><td>GND</td><td>51</td></tr><tr><td>23</td><td>S</td><td>S</td><td>50</td></tr><tr><td>24</td><td>S</td><td>S</td><td>49</td></tr><tr><td>25</td><td>GND</td><td>GND</td><td>48</td></tr><tr><td>26</td><td>S</td><td>S</td><td>47</td></tr><tr><td>27</td><td>S</td><td>S</td><td>46</td></tr><tr><td>28</td><td>GND</td><td>GND</td><td>45</td></tr><tr><td>29</td><td>SB</td><td>SB</td><td>44</td></tr><tr><td>30</td><td>SB</td><td>SB</td><td>43</td></tr><tr><td>31</td><td>SB</td><td>SB</td><td>42</td></tr><tr><td>32</td><td>SB</td><td>SB</td><td>41</td></tr><tr><td>33</td><td>SB</td><td>SB</td><td>40</td></tr><tr><td>34</td><td>SB</td><td>SB</td><td>39</td></tr><tr><td>35</td><td>SB</td><td>SB</td><td>38</td></tr><tr><td>36</td><td>SB</td><td>SB</td><td>37</td></tr></table>						CIRCUIT #			CIRCUIT #	1	GND	GND	72	2	S	S	71	3	S	S	70	4	GND	GND	69	5	S	S	68	6	S	S	67	7	GND	GND	66	8	S	S	65	9	S	S	64	10	GND	GND	63	11	S	S	62	12	S	S	61	13	GND	GND	60	14	S	S	59	15	S	S	58	16	GND	GND	57	17	S	S	56	18	S	S	55	19	GND	GND	54	20	S	S	53	21	S	S	52	22	GND	GND	51	23	S	S	50	24	S	S	49	25	GND	GND	48	26	S	S	47	27	S	S	46	28	GND	GND	45	29	SB	SB	44	30	SB	SB	43	31	SB	SB	42	32	SB	SB	41	33	SB	SB	40	34	SB	SB	39	35	SB	SB	38	36	SB	SB	37	REV.	DESCRIPTION		DESIGN		APPROVAL		DATE	
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